

X-1002 US  
10/082,518

PATENT



IN THE UNITED STATES PATENT OFFICE

Applicant: L. James Hwang et al.  
Assignee: Xilinx, Inc.  
Title: METHOD AND SYSTEM FOR INTEGRATING CORES IN FPGA-BASED SYSTEM-ON-CHIP (SoC)  
Serial No.: 10/082,518 Filed: February 22, 2002  
Examiner: Whitmore, Stacy A. Art Unit: 2812  
Docket No.: X-1002 US Conf. No. 5974

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AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

To whom it may concern:

Responsive to the Office Action dated March 24, 2004,  
please amend the above-identified application as follows: